


IN	OUT
1	1
2	2
3	3
4	4
5	5
6	6
7	7
8	8
9	9
10	10
11	11
12	12
13	13
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34	34

P1 is connected to TP1 via discrete resistors to allow series or parallel terminations
 P1 is connected to TP2 via direct resistor packs to allow straight through signaling
 If RP's not installed then TP1 wire-wrapped to TP2 to add second connector and Terminal Block
 Traces routed from IN to TP to TB to OUT with differential pairs based on SCSI standard

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