DYNAMIC ENGINEERING

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Est. 1988

Statement of Volatility
PN: IP-Relay16

Manufacturer Part Number: IP-Relay16, includes all options (-ROHS etc.)
Manufacturer Part Description: IndustryPack Module with 16 SPDT Relays
Memory Type: CPLD built in programming
Memory Size: CPLD Xilinx XC95XXX series: Internal registers for configuration of Relays.

FLASH : Memory to provide power on configuration to CPLD. Not user accessible, built into CPLD.

Volatility: FLASH is non-volatile.   User Registers are volatile.
User Accessible:. Registers are tied to controls for Relays etc. User accessible, but only control not used for user data.
Clearing Procedure: Use and Power cycle for Register memory.   To clear the FLASH a standard 3 pass write cycle would be required.
Notes or Warnings: None