

DYNAMIC ENGINEERING

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Statement of Volatility cPCI3U32B1LPCIeX4

Manufacturer Part Number: cPCl3U32B1LPCleX4, includes all options Manufacturer Part Description: cPCl carrier, 1 PCle position with 32/33 up to 32/66 PCl side, 1 lane PCle connection to PCle Device, local power supplies and user features.

Memory Type: Bridge, EEPROM

Memory Size: Pericom PI7C9X111SL: Internal registers for configuration, FIFO memory to support traffic. EEPROM = 256x8.

Volatility: FIFO memory is continuously rewritten during operation and effectively cleared by this process. EEPROM for storing Bridge operational parameters. EEPROM is only able to store Bridge settings. No user operational data pass through EEPROM.

EEPROM is not required for operation and can be left off/removed from standard build if desired. -NEEP option.

User Accessible: FIFO within Bridge is not user accessible in the traditional sense as the PCI/PCIe traffic is flowing through these memories without user control.

Clearing Procedure: Use and Power cycle for FIFO memory.

Notes or Warnings: None