



DYNAMIC ENGINEERING

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Est. 1988

Statement of Volatility

PN: DESWBO

Manufacturer Part Number: DESWBO, includes all types

Manufacturer Part Description: Dynamic Engineering SpaceWire Break-Out (DESWBO) monitors the signals of a single SpaceWire link.

Memory Type: PROM/FLASH, SRAM. DESWBO will have either PROM or FLASH for loading not both.

Memory Size: varies with installed FPGA

Volatility: SRAM cleared by power cycle. PROM/FLASH is retained through power cycle. PROM/FLASH is not accessible by user for writing or read-back.

PROM/FLASH is used to reload FPGA at power-on.

User Accessible: SRAM within FPGA is configured into registers etc. SRAM contents are lost at power down and re-written with base design from FLASH upon power-on.

Clearing Procedure: Power cycle.

Notes or Warnings: None