



## DYNAMIC ENGINEERING

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# Statement of Volatility

## PN: PCI-ECL-II

**Manufacturer Part Number:** PCI-ECL-II, includes all options (-NECL,-PECL etc.)

**Manufacturer Part Description:** PCI based adapter with Spartan 6 FPGA, PCI interface and IO based on ECL [PECL,NECL]

**Memory Type:** Block RAM within FPGA, SDRAM, FLASH

**Memory Size:** Spartan 6 100 Block RAM compliment, SDRAM:32Mx16 [16Mx16x2], FLASH:XCF32PV0G48

**Volatility:** FLASH is retained during power cycle, SDRAM and FPGA Block RAM are cleared by power cycle.

**User Accessible:** FLASH is not user accessible, SDRAM and FPGA Block RAM are used during operation.

**Clearing Procedure:** Use Power cycle for FPGA and SDRAM. To clear the FLASH the external IMPACT USB adapter and SW are used.

**Notes or Warnings:** None